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The diagram shows a 16-bit bus system. At the top, a horizontal line represents the bus, with 16 small vertical lines indicating the data width. Below the bus, there are two 8-bit memory modules, each represented by a rectangle with 8 horizontal lines. To the right of the bus, there are two 8-bit processors, each represented by a rectangle with 8 horizontal lines. The bus is connected to the memory modules and the processors.

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